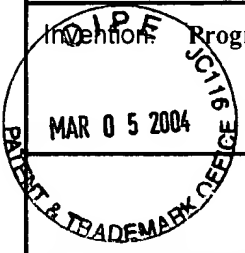


2189 \$

<b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b>			Docket No. 871.0013.USU
Applicant(s): Zhao et al.			

Serial No. 09/823,159	Filing Date 3/30/2001	Examiner Patel, Nimesh G.	Group Art Unit 2189
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Invention: Programmable CPU/Interface Buffer Structure Using Dual Port Ram

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TO THE COMMISSIONER FOR PATENTS:

Technology Center 2100

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT		RATE	ADDITIONAL FEE
TOTAL CLAIMS	20 -	20 =	0	x	\$18.00	\$0.00
INDEP. CLAIMS	5 -	3 =	2	x	\$86.00	\$172.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>						\$0.00
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>						<b>\$172.00</b>

- ☐ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_
- ☒ A check in the amount of **\$172.00** to cover the filing fee is enclosed.
- ☒ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. **50-1924**
  - ☒ Any additional filing fees required under 37 C.F.R. 1.16.
  - ☐ Any patent application processing fees under 37 CFR 1.17.

Signature

Dated: **March 3, 2004**

Customer No. 29683  
 Harry F. Smith (Reg. No. 32,493)  
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 (203) 925-9400

I certify that this document and fee is being deposited on <b>3/3/04</b> with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
Signature of Person Mailing Correspondence	
<b>Elaine F. Mian</b> Typed or Printed Name of Person Mailing Correspondence	

cc:



IN THE U.S. PATENT AND TRADEMARK OFFICE

*Handwritten signature*  
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MAR 09 2004  
Technology Center 2100

In re U.S. Patent Application of:

APPLICANT: Zhao et al.

SERIAL NO.: 09/823,159

FILING DATE: 03/30/2001

EXAMINER: Patel, Nimesh G.

ART UNIT: 2189

ATTORNEY'S DOCKET NO.: 871.0013.USU

TITLE: PROGRAMMABLE CPU/INTERFACE BUFFER STRUCTURE USING DUAL  
PORT RAM

Commissioner for Patents  
Alexandria, VA 22313

**RESPONSE TO OFFICE ACTION**

Sir:

This paper is herewith filed in response to the Examiner's Office Action mailed on 12/03/2003 for the above-captioned U.S. Patent Application. This paper is deemed to be filed within the shortened statutory period and no petition or fee for an extension of time is required. However, should the undersigned attorney be mistaken, please consider this a petition for any extension of time that may be required to maintain the pendency of this Patent Application, and charge deposit account no.: 50-1924 for any required fee deficiency.

Please amend the application as shown below.

03/08/2004 WABDELRI 00000031 09823159  
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172.00 OP